

2661
JPR

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Bendak et al) Art Unit: 2661
Serial No.: 09/747,380) Examiner: K. Vanderpuye
Filed: 12/22/2000) Docket No. AMCC4860
For: **SYSTEM AND METHOD FOR PROGRAMMING THE BIT ERROR RATE OF
FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL DIGITAL
FRAME STRUCTURE**

CERTIFICATION UNDER 37 CFR § 1.8

I hereby certify that the documents referred to as enclosed herein are being deposited with the United States Postal Service as first class mail on this date December 8, 2004, in an envelope addressed to:
Commissioner for Patents, Box 1450, Alexandria, VA. 22313-1450

December 8/2004
Date

Torraine A. Mead
Signature

MAIL STOP: AMENDMENT
Commissioner for Patents
P. O. Box 1450
Alexandria, VA. 22313-1450

AMENDMENT AND REQUEST FOR RECONSIDERATION

In response to the Office Action dated September 8, 2004, Applicant requests amendment of the application as indicated below. All pending claims, including unchanged claims, are included below.